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PCB Design Rules

Properly designing your circuit board can help reduce your manufacturing cost. Many times designers used unnecessary tight tolerances that drive up the cost of their circuit boards. Below are design rule guidelines that will help you stay within our tolerances. This will help save unnecessary redesign and manufacturing costs.

Tolerances	Standard	Premium
Drill to copper feature	.009	007
Aspect Rati	8:1	10:1
Annular ring over drill		
– inner layer	.005	.003
– outer layer	.005	.003
Clearance pad over drill	.020	.016
Copper feature to edge	.010	.005
True position drill	.002	.0015
True position of artwork	.003	.002
Route	+/005	+/002
Thickness (minimum +/003)	+/-10%	+/-8%
Etch tolerance	.001	.0007
Impedance	10%	5%
Soldermask thickness (LPI)	.00050007	
Minimum silkscreen feature	.008	.006
Minimum character size legend	.050X.050	.040X.040